**COURSES COMPLETED**

* Operating System
* C++ programming
* Java Programming
* Python
* Html
* CSS
* JS

**PROJECTS**

Organisation: College Type: Project

Project: Low voltage Bandgap Reference Circuit using 45nm CMOS

Description:Using the concept of PTAT and CTAT circuit,we build a circuit

whose output is independent of temperature and supply

voltage(independent voltage reference circuit),and it can be used in

integrated circuit for which fixed low voltage is required.

Project :Notes taking website using HTML,CSS and JS

URL-https://manish6grd.github.io/notes-taking-website/

**ACHIEVEMENTS & AWARDS**

* Represented school in Chess competition
* Represented school in Cricket competition
* Winner of designing workshop held at COSS INDIA

**PERSONAL DETAILS**

Date of Birth:15/10/1995

Father Name:Dilip Kumar Verma

Nationality: Indian

Languages Known: English, Hindi

**TECHNICAL SKILLS**

* Testing and debugging codes
* Languages: C, C++, JAVA,Python,HTML,CSS,JS

**CONTACT**

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📱 7759949718

**REFERENCES**

R.Elumalai

HOD, ECE, CMRIT

9448826813

Chetan.H

Prof, ECE, CMRIT

9916012544

**EDUCATION**

* BE, ECE from CMRIT COLLEGE, CGPA 6.3/10
* 10+2 from BNS DAV School in 2016, Percentage 79.8%
* 10th from BNS DAV School in 2014, CGPA 9.2/10

**PROFILE SUMMARY**

An Enthusiastic candidate seeking for a best opportunity to learn and explore new technologies, and contribute cent percent dedication to the company

**MANISH KUMAR**

**7759949718**

**Manish6grd@gmail.com**